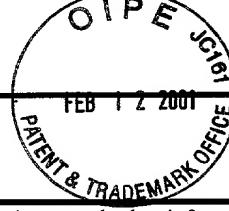


02-13-01

JC06 Rec'd PCT/PTO 12 FEB 2001

FORM PTO-1390 (Modified) (REV 10-95)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY'S DOCKET NUMBER
TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371				139/02059
INTERNATIONAL APPLICATION NO. PCT/IL98/00393		INTERNATIONAL FILING DATE August 18, 1998		U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 09/762945
TITLE OF INVENTION INSPECTION OF PRINTED CIRCUIT BOARDS USING COLOR		PRIORITY DATE CLAIMED		
APPLICANT(S) FOR DO/EO/US Ronald F. KAMINSKY et al.				
<p>Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:</p> <ol style="list-style-type: none"> 1. <input checked="" type="checkbox"/> This is a FIRST submission of items concerning a filing under 35 U.S.C. 371. 2. <input type="checkbox"/> This is a SECOND or SUBSEQUENT submission of items concerning a filing under 35 U.S.C. 371. 3. <input checked="" type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1). 4. <input checked="" type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date. 5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371 (c) (2)) <ol style="list-style-type: none"> a. <input checked="" type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau). b. <input checked="" type="checkbox"/> has been transmitted by the International Bureau. c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US). 6. <input type="checkbox"/> A translation of the International Application into English (35 U.S.C. 371(c)(2)). 7. <input checked="" type="checkbox"/> A copy of the International Search Report (PCT/ISA/210). 8. <input type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3)) <ol style="list-style-type: none"> a. <input type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau). b. <input type="checkbox"/> have been transmitted by the International Bureau. c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired. d. <input type="checkbox"/> have not been made and will not be made. 9. <input type="checkbox"/> A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)). 10. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)). 11. <input type="checkbox"/> A copy of the International Preliminary Examination Report (PCT/IPEA/409). 12. <input type="checkbox"/> A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)). <p>Items 13 to 18 below concern document(s) or information included:</p> <ol style="list-style-type: none"> 13. <input checked="" type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98. 14. <input checked="" type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included. 15. <input checked="" type="checkbox"/> A FIRST preliminary amendment. <input checked="" type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment. 16. <input type="checkbox"/> A substitute specification. 17. <input type="checkbox"/> A change of power of attorney and/or address letter. 18. <input type="checkbox"/> Certificate of Mailing by Express Mail 19. <input checked="" type="checkbox"/> Other items or information: <p>Amendments to the claims of the International Application under PCT Article 34 are transmitted herewith as annexes to the IPER.</p>				

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 097762945	INTERNATIONAL APPLICATION NO. PCT/IL98/00393	ATTORNEY'S DOCKET NUMBER 139/02059
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20. The following fees are submitted:		CALCULATIONS PTO USE ONLY	
BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) :		\$860.00	
<input checked="" type="checkbox"/> Search Report has been prepared by the EPO or JPO		\$930.00	
<input type="checkbox"/> International preliminary examination fee paid to USPTO (37 CFR 1.482)		\$720.00	
<input type="checkbox"/> No international preliminary examination fee paid to USPTO (37 CFR 1.482) but international search fee paid to USPTO (37 CFR 1.445(a)(2))		\$790.00	
<input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO		\$1,070.00	
<input type="checkbox"/> International preliminary examination fee paid to USPTO (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(2)-(4)		\$98.00	
ENTER APPROPRIATE BASIC FEE AMOUNT =		\$860.00	
Surcharge of \$130.00 for furnishing the oath or declaration later than months from the earliest claimed priority date (37 CFR 1.492 (e)).		<input type="checkbox"/> 20	<input type="checkbox"/> 30
		\$0.00	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE
Total claims	23 - 20 =	3	x \$18.00 \$54.00
Independent claims	2 - 3 =	0	x \$80.00 \$0.00
Multiple Dependent Claims (check if applicable).		<input type="checkbox"/> \$0.00	
TOTAL OF ABOVE CALCULATIONS =		\$914.00	
Reduction of 1/2 for filing by small entity, if applicable. Verified Small Entity Statement must also be filed (Note 37 CFR 1.9, 1.27, 1.28) (check if applicable).		<input type="checkbox"/>	\$0.00
SUBTOTAL =		\$914.00	
Processing fee of \$130.00 for furnishing the English translation later than months from the earliest claimed priority date (37 CFR 1.492 (f)).		<input type="checkbox"/> 20	<input type="checkbox"/> 30 + \$0.00
TOTAL NATIONAL FEE =		\$914.00	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) (check if applicable).		<input type="checkbox"/>	\$0.00
TOTAL FEES ENCLOSED =		\$914.00	
		Amount to be: refunded	\$
		charged	\$

A check in the amount of _____ to cover the above fees is enclosed.

Please charge my Deposit Account No. **03-3419** in the amount of **\$914.00** to cover the above fees.
A duplicate copy of this sheet is enclosed.

The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. **03-3419** A duplicate copy of this sheet is enclosed.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:

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SIGNATURE

Paul FENSTER

NAME

33,877

REGISTRATION NUMBER

February 7, 2001

DATE

139/02059 A01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: R. KAMINSKY, et al.
Serial Number: Not Yet Assigned
Filed: 18 August 1998 as PCT/IL98/00393
For: INSPECTION OF PRINTED CIRCUIT BOARDS USING COLOR
Art Unit: Not Yet Assigned
Examiner: Not Yet Assigned

Honorable Commissioner of Patents and Trademarks
Washington DC 20231

PRELIMINARY AMENDMENT

Sir:

Further to the concurrent filing of the U.S. national stage of PCT/IL98/00393, kindly amend the application as follows prior to examination:

IN THE DISCLOSURE

Kindly add, on page 1, after the title:

--RELATED APPLICATION

The present application is a U.S. national stage application of PCT/IL98/00393, filed 18 August 1998.--

IN THE CLAIMS

The following amendments are based on the claims attached to the IPER submitted together with the application.

Claim 3, line 1, delete "or claim 2".

Claim 4, line 1, delete "or claim 2".

Claim 6, line 1, delete "or claim 5".

Claim 8, line 1, delete "or claim 7".

Claim 9, line 1, change "any of claims 6-8" to --claim 6--.

Claim 11, line 1, delete "or claim 10".

Claim 12, line 1, change "any of claims 6-11" to --claim 6--.

Claim 13, line 1, change "any of claims 6-12" to --claim 6--.

Claim 17, line 1, change "any of claims 2-16" to --claim 2--.

Claim 18, line 1, change "any of claims 4, 5 or 12" to --claim 4--.

Claim 21, line 1, delete "or claim 20".

Claim 22, line 1, change "any of claims 19-21" to --claim 19--.

Kindly add the following new claim:

--23. A method according to claim 12 and comprising:
determining a color gamut characteristic of an oxide; and
comparing the color values of a pixel to the determined gamut to determine if the pixel is
an oxide.--

REMARKS

The present application is a U.S. national application of PCT/IL98/00393. Applicant respectfully requests that the claims attached to the enclosed International Preliminary Examination Report be used as the basis for the present amendments.

The present application, after amendment, contains claims 1-23. The IPER claims in the application have been amended to place them in proper form for examination in the US. New claim 23 has been added to better define the invention as described in the application as originally filed.

An examination on the merits is respectfully awaited.

Respectfully submitted,
R. KAMINSKY, et al.



Paul FENSTER
Registration No. 33,877

February 7, 2001
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INSPECTION OF PRINTED CIRCUIT BOARDS USING COLOR

FIELD OF THE INVENTION

The present invention is related to the field of inspection of patterned surfaces, such as printed circuit boards and in particular to the identification of certain types of conditions, such as conductor oxidation, using color.

BACKGROUND OF THE INVENTION

One widely used method for inspection of "bare" printed circuit boards having metalized portions and, unmetalized, laminate portions is to:

- (a) illuminate the board with light which appears to come from a wide range of angular directions;
- (b) image the illuminated portion;
- (c) define the metalized portions from the image of the board; and
- (d) determine defects in metalization on the board by one or more of comparison of the image with a standard image and analysis of the image with a set of standards or rules to determine if it meets the rules.

Among prior art publications which describe such methods are US Patents 4,758,888; 5,619,429; 5,774,572; and 5,774,573, the disclosures of which are incorporated herein by reference.

One of the problems in making this comparison is the determination of edges between areas covered by conductors on the board and areas without conductors. A number of publications, including, for example, US Patent 5,524,152, the disclosure of which is incorporated herein by reference, have suggested the use of color to improve this determination. In general, these methods image the board utilizing a color at which the contrast between the conductors and areas from which the conductors have been removed is maximized. This may be achieved by illuminating the surface with light which maximizes contrast or by filtering the light which is reflected prior to forming the image. In particular, the above referenced US Patent 5,524,152 generates one or more different images each based on a different filter to emphasize different materials, such as gold copper, etc.

US Patent 5,483,603, the disclosure of which is incorporated by reference, defines a region in RGB space that characterizes a conductor and a region in RGB space that characterizes a laminate. Pixels are classified as conductors or laminate depending on which regions contains their RGB values.

While ideally, the board should consist of only two types of areas, namely metal and bare substrate, unfortunately the metalization on a board is sometimes oxidized. Furthermore, several types of oxidation and various levels of severity of oxidation exist, making it difficult to determine criteria for reliably identifying oxidation.

5 Since oxidation is a widespread phenomena in printed circuit board manufacture, especially when the boards have been in storage for a considerable period of time and/or have been stored under less than ideal conditions, it would be useful to be able to reliably determine areas of oxidation on printed circuit boards. Such identification could be useful, for example, in avoiding classification of such areas as containing defects.

10 SUMMARY OF THE INVENTION

In a broad aspect of the invention, the present invention determines the presence of oxide on a conductor on a printed circuit board from an image of the printed circuit board.

15 In accordance with some preferred embodiments of the invention, color characteristics of light reflected from the surface of a printed circuit board are used to determine if the area is an oxidized conductor. The method of the present invention may be used in any imaging system in which images at multiple colors are acquired.

20 According to one broad aspect of the present invention, the presence of an oxide (or another definable aspect of the pixel) is determined by comparing the color characteristics of a suspect pixel with the color characteristics of either the metalization or laminate portions, preferably, the metal portions.

According to a second broad aspect of the present invention, the presence of an oxide (or another definable aspect of the image) is determined by comparing the color components of a suspect pixel utilizing a normalizing scale based on the color characteristics of either the metalization or laminate portions, preferably, the metal portions.

25 According to a third broad aspect of the present invention, the presence of an oxide is determined by first finding a region of the color space which is characteristic of oxide and then determining if the color values of a pixel is within this region.

30 In accordance with one aspect of some preferred embodiments of the invention, the determination of an oxide is made without directly excluding the presence of a laminate. In addition, in some preferred embodiments of the invention, only "interior" pixels (that is pixels not near an edge) are classified as oxide.

In a preferred embodiment of the invention each of the color scales for RGB is first normalized between a low light level and the reflection from fine (unoxidized) copper.

Preferably, the "dark level" is set at a few gray levels above zero and the "copper level" is set at a few gray levels below the maximum gray level. For example, the darkest portion of the image may be set at 10 gray levels and the copper (brightest) gray level set at 230 gray levels for 8 bit quantification of light level.

5 In a preferred embodiment of the invention, a pixel having a red intensity below a red threshold level is determined not to be an oxide. For other pixels, for which not all of the colors have intensities within a range indicative of copper, preferably the red level is compared to the level of other colors. If the red (normalized) level is greater than both the green (normalized) and blue (normalized) levels, the pixel is identified as brown oxide, the most 10 prevalent type. Different combinations of characteristics may be used for determining the presence of other oxide types.

In other preferred embodiments of the invention, a comparison is made with brightness levels of the laminate.

15 Similar comparisons are useful for determining other types of oxidation and/or laminate problems and may be useful for the determination of the presence of other materials such as gold, tin-lead, or double treated copper conductors, cyanide ester, polymid or teflon laminate, photoresist residues or dust.

There is thus provided, in accordance with a preferred embodiment of the invention, a method of analysis of a printed circuit board comprising:

20 generating an image of the printed circuit board; and
determining the presence of an oxide from an analysis of the image.

Preferably, determination of the presence of an oxide is made without determining whether the pixel is a laminate pixel.

25 Preferably, generating an image comprises generating a pixelated image having brightness values for each pixel and determining the presence of the of an oxide comprises determining the presence of the oxide responsive to the brightness values.

30 Preferably, generating an image comprises generating a plurality of images each at a different color and having brightness values for each pixel in each image and determining the presence of an oxide includes making the determination based on an analysis of the pixel values in at least two of the images. Preferably, the plurality of images comprises a red, a green and a blue image.

Preferably, determination of the presence of an oxide includes eliminating pixels from consideration based on a brightness value for a single color. Preferably the color is red and

pixels having a red brightness level below a given value are eliminated from consideration as being an oxide. Preferably, the color is red and wherein pixels having a red value above a given value are eliminated from consideration as being an oxide.

In a preferred embodiment of the invention, determination of the presence of an oxide includes eliminating pixels from consideration based on a comparison between the brightness level of two colors. Preferably, the two colors are red and green and wherein the pixel is eliminated if its red brightness value compared to that of copper is less than its green brightness value compared to copper. Preferably, the two colors are red and blue and wherein the pixel is eliminated if its red brightness value compared to that of copper is less than its blue brightness value compared to copper.

In a preferred embodiment of the invention, determination of the presence of an oxide includes eliminating pixels from consideration based on an analysis of the brightness levels of three colors.

In a preferred embodiment of the invention, determination of the presence of an oxide includes eliminating pixels from consideration based on a comparison between the brightness level of three colors with brightness levels for copper. Preferably, a pixel is eliminated from consideration as an oxide when its color brightness values have a Mahalanobis distance greater than a given value from the mean values of the brightness values for copper. Preferably, the given value for the Mahalanobis distance is between 4 and 8, more preferably, about 6.

In a preferred embodiment of the invention, the determination of the presence of an oxide is made based on a relationship between the brightness values of the image and brightness values characteristic of copper.

Preferably, the method includes:

determining a color gamut characteristic of the oxide; and

comparing the color values of a pixel to the determined gamut of values to determine if the pixel is an oxide.

There is further provided, in accordance with a preferred embodiment of the invention, a method of multi-color image processing, comprising:

generating a plurality of images of an object, said images being acquired at different wavelength ranges;

determining gradient values in the image; and

analyzing the multi-color images to determine characteristics of the object,

wherein regions of the image at or near high gradients are analyzed differently from other regions of the image.

In preferred embodiments of the invention, the regions of high gradient are transition regions, such as edge regions and in particular edges between conductors and laminate.

5 In preferred embodiments of the invention, the regions of high gradient are excluded from analysis.

In preferred embodiments of the invention, the object is a printed circuit board.

The invention will be more clearly understood from the following description of non-limiting preferred embodiments thereof.

10 DESCRIPTION OF PREFERRED EMBODIMENTS OF THE INVENTION

In a preferred embodiment of the invention, a printed circuit board (PCB) is illuminated by multi-wavelength light and imaged in a plurality of colors, for example in RGB. While the methods of the invention are applicable to any imaging system in which images at multiple colors are acquired, in an especially preferred embodiment of the invention, 15 the PCB is illuminated by and the images are acquired by the apparatus described in PCT patent application PCT/IL98/00285, filed June 15, 1998, the disclosure of which is incorporated by reference.

In a preferred embodiment of the invention in which it is desired to determine the presence of brown oxide, three color images, namely R, G and B images are generated.

20 Optionally, as disclosed in PCT/IL98/00285, filters are used to reduce any cross-over of colors between the images, such that each image represents reflections in a different wavelength. Alternatively, white light is used for illumination and standard light division (and associated overlapping sensitivity) is used.

As a first preferred step in the determination, a set-up is performed to calibrate the 25 system. This calibration is preferably performed utilizing a standard manufactured, bare, PCB, preferably free of oxide. Preferably, an operator chooses a portion of the board which has a mix of interior copper pixels, edge pixels and some laminate pixels.

First, the brightness of the illumination and the camera and A/D parameters are 30 adjusted such that the gray level for each of the colors RGB for subsequently acquired images will have a dark level of about 10 and a value for a copper pixel of about 230 for 8 bit quantification of light level.

The next step is to define a "low sure" threshold from an analysis of transition pixels (pixels on or near transitions between conductor and laminate). This calculation is in

accordance with the calculation detailed in Fig. 15 from US Patent 5,774,573 (Caspi et al.) except that only the gray level value is accumulated, not ordered pairs of (gray level, gray level difference). The object of this threshold is to define pixels whose red level is so low that they cannot be an oxide. Other thresholds and methods of defining such thresholds may be used 5 instead of the "low-sure" threshold.

The next step in the set-up is to produce a 3-D histogram of the colors of all the interior copper pixels in the region of limited size. It has been found that a region of 700 by 700 pixels gives adequate statistics. For the 3-D histogram, the mean is computed. The mean may also be computed from the three separate color distributions.

10 Preferably, a covariance matrix is formed from the values. This matrix is:

$$\begin{array}{lll} \sum \frac{(R_i - \bar{R})^2}{(N-1)} & \sum \frac{(R_i - \bar{R}) \bullet (G_i - \bar{G})}{(N-1)} & \sum \frac{(R_i - \bar{R}) \bullet (B_i - \bar{B})}{(N-1)} \\ \sum \frac{(R_i - \bar{R}) \bullet (G_i - \bar{G})}{(N-1)} & \sum \frac{(G_i - \bar{G})^2}{(N-1)} & \sum \frac{(G_i - \bar{G}) \bullet (B_i - \bar{B})}{(N-1)} \\ \sum \frac{(R_i - \bar{R}) \bullet (B_i - \bar{B})}{(N-1)} & \sum \frac{(G_i - \bar{G}) \bullet (B_i - \bar{B})}{(N-1)} & \sum \frac{(B_i - \bar{B})^2}{(N-1)} \end{array}$$

This matrix is inverted to form an inverted covariance matrix. This inverted matrix is used to determine if a pixel is an oxide.

15 In a preferred embodiment of the invention, the following rules are applied to image pixels in a PC board under test to determine if a pixel is oxidized.

1) if the pixel is at a strong gradient (for example, it is an edge pixel), it is not checked and is not marked as an oxide;

20 2) if is Mahalanobis distance (as defined below) from the mean value of copper pixels is less than some given value, it is not an oxide; This value may vary between about 4 and 8, with 6 being a preferred value;

3) if the red pixel value is less than the low-sure threshold for red, it is not classified as an oxide (it is definitely laminate);

4) optionally, if red is less than green for a pixel, it is not classified as an oxide;

5) optionally, if red is less than blue for a pixel, it is not classified as an oxide; and

25 6) optionally, if the red value is greater than some high value (for example, 220) it is not classified as an oxide.

The Mahalanobis distance for a pixel is determined by forming a vector and pre-multiplying the inverted covariance matrix by the vector and then post-multiplying the product

by the transpose of the vector. The scalar that results from this "modified inner product" of the vector is the square of the Mahalanobis distance .

A preferred algorithm for determining if a pixel is at a strong gradient (i.e., it is considered to be an edge pixel and thus not subject to the above analysis) is:

5 Let $I[x,y]$ be the gray level value of the red image at location (x,y) .

To calculate if the pixel at location (x,y) is an edge pixel:

Let $s = \text{ceo}(I[x-2,y], I[x-1,y], I[x,y], I[x+1,y], I[x+2,y])$
 10 $+ \text{ceo}(I[x,y-2], I[x,y-1], I[x,y], I[x,y+1], I[x,y+2])$
 $+ \text{ced}(I[x-2,y-2], I[x-1,y-1], I[x,y], I[x+1,y+1], I[x+2,y+2])$
 $+ \text{ced}(I[x-2,y+2], I[x-1,y+1], I[x,y], I[x+1,y-1], I[x+2,y-2])$

where the functions $\text{ceo}()$ and $\text{ced}()$ are defined by the computer program fragment below.

Then if $s \geq 12$, the pixel at (x,y) is an edge pixel.

```
> typedef unsigned char uchar;
>
> static int sst[4][4] = { {1, 2, 2, 2}, {2,-1, 2, 2},
>                         {2, 2, 0, 0}, {2, 2, 0, 0} };
>
> int f(int d)
> {
>   return (abs(d) < 4 ? 0 : (abs(d) < 8 ? 1 :
>     (abs(d) < 12 ? 2 : (abs(d) < 16 ? 3 :
>       (abs(d) < 24 ? 4 : (abs(d) < 36 ? 5 :
>         (abs(d) < 128 ? 6 : 7)))))) * (d < 0 ? -1 : 1);
> }
>
> void ht(float d1, float d2, int *s, float *v)
> {
>   *v = 0.375*d1 + 0.125*d2 ;
>
>   if( fabs(*v) < 0.3 )
>     *s = (fabs(d1) < 0.1) ? 2 : 3 ;
>   else {
>     *s = (*v>0) ? 0 : 1 ;
>   }
>   *v = fabs(*v);
> }
>
> int ct(int d[4])
> {
>   int s, s1, s2;
>   float v1, v2, ret;
>   int t, v;
```

```

>
> ht(-d[1], -d[0], &s1, &v1);
> ht(d[2], d[3], &s2, &v2);
>
5  > ret = (v1+v2+1)/2 ;
> if (ret > 3)
>     ret = 3;
> if (ret < -3)
>     ret = -3;
10 > v = (int) ret ;
>
> s = sst[s1][s2];
>
> if (s == 2) {
15 >     if (fabs(v1) > 2 || fabs(v2) > 2)
>         t = 4;
>     else
>         t = 0;
> }
20 > else
>     t = 0;
>
> return t;
> }
25 >
> int ceo(uchar g0, uchar g1, uchar g2, uchar g3, uchar g4)
> {
>     int d[4];
>
30 >     d[0] = f(g1-g0);
>     d[1] = f(g2-g1);
>     d[2] = f(g3-g2);
>     d[3] = f(g4-g3);
>     return ct(d);
35 > }
>
> int ced(uchar g0, uchar g1, uchar g2, uchar g3, uchar g4)
> {
>     int d[4];
40 >
45 >     d[0] = f((g1-g0)/sqrt(2));
>     d[1] = f((g2-g1)/sqrt(2));
>     d[2] = f((g3-g2)/sqrt(2));
>     d[3] = f((g4-g3)/sqrt(2));
>     return ct(d);
> }

```

In an alternate preferred embodiment of the invention, a multi-dimensional region of color space characteristic of an oxide, whose presence is to be tested, is determined. Such a determination may be made by imaging a number of samples of oxide and determining the

range, in color space, of the images. During testing, the color values of test pixels are compared to the values in the determined region. If the values are within the region, the pixel is labeled as an oxide. Optionally, edge and other high gradient pixels are excluded from classification as oxides. While such color maps have been used, in the past, to determine metal and laminate, they have not been used to determine the presence of oxide and have not been used in conjunction with gradient information. Furthermore, this method is easily adapted to testing for other types of oxide and even to the presence of dust on conductors.

One preferred use of the information determined by the above described method is in avoiding the classification oxide or dust pixels as faults in a printed circuit board. Since these pixels generally have a brightness between that of copper and laminate, they may sometimes be classified as laminate by normal threshold programs. Edge finding programs may also have difficulty in determining edges near oxide. Thus, when the above method determines that a pixel is oxide, it is, for the purposes of a fault determination program, defined as a metal pixel.

The present invention has been described in conjunction with preferred, non-limiting, embodiments thereof. It should be understood that variations on these embodiments, within the scope of the invention, as defined by the following claims, will occur to persons of skill in the art. In the following claims, where the words "comprising," "comprises," "including" or "includes" are used these words mean that the apparatus or method includes the following structure or steps, but may (but need not) include other structure or steps.

CLAIMS

1. A method of analysis of a printed circuit board comprising:
generating an image of the printed circuit board; and
determining the presence of an oxide from an analysis of the image.
- 5
2. A method according to claim 1 wherein determination of the presence of an oxide at a particular pixel of the image is made without determining whether the pixel is a laminate pixel.
- 10
3. A method according to claim 1 or claim 2 wherein generating an image comprises generating a pixelated image having brightness values for each pixel and wherein determining the presence of the of an oxide comprises determining the presence of the oxide responsive to the brightness values.
- 15
4. A method according to claim 1 or claim 2 wherein generating an image comprises generating a plurality of images each at a different color and having brightness values for each pixel in each image and wherein determining the presence of an oxide includes making the determination based on an analysis of the pixel values in at least two of the images.
- 20
5. A method according to claim 4 wherein the plurality of images comprises a red, a green and a blue image.
- 25
6. A method according to claim 4 or claim 5 wherein determination of the presence of an oxide includes eliminating pixels from consideration based on a brightness value for a single color.
- 30
7. A method according to claim 6 wherein the color is red and wherein pixels having a red brightness level below a lower given value are eliminated from consideration as being an oxide.
8. A method according to claim 6 or claim 7 wherein the color is red and wherein pixels having a red value above an upper given value are eliminated from consideration as being an oxide.

9. A method according to any of claims 6-8 wherein determination of the presence of an oxide includes eliminating pixels from consideration based on a comparison between the brightness level of two colors.

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10. A method according to claim 9 wherein the two colors are red and green and wherein the pixel is eliminated if its red brightness value compared to that of copper is less than its green brightness value compared to copper.

10 11. A method according to claim 9 or claim 10 wherein the two colors are red and blue and wherein the pixel is eliminated if its red brightness value compared to that of copper is less than its blue brightness value compared to copper.

12. A method according to any of claims 6-11 wherein determination of the presence of an
15 oxide includes eliminating pixels from consideration based on an analysis of the brightness levels of three colors.

13. A method according to any of claims 6-12 wherein determination of the presence of an oxide includes eliminating pixels from consideration based on a comparison between the
20 brightness level of three colors with brightness levels for copper.

14. A method according to claim 13 wherein a pixel is eliminated from consideration as an oxide when its color brightness values have a Mahalanobis distance greater than a given value from the mean values of the brightness values for copper.

25

15. A method according to claim 14 wherein the given value for the Mahalanobis distance is between 4 and 8.

16. A method according to claim 14 wherein the given value for the Mahalanobis distance
30 is about 6.

17. A method according to any of claims 2-16 wherein the determination of the presence of an oxide is made based on a relationship between the brightness values of the image and brightness values characteristic of copper.

5 18. A method according to any of claim 4, 5 or 12 and comprising:
determining a color gamut characteristic of the oxide; and
comparing the color values of a pixel to the determined gamut of values to determine if the pixel is an oxide.

10 19. A method of multi-color image processing, comprising:
generating a plurality of images of an object, said images being acquired at different wavelength ranges;
determining gradient values of the intensity in an image; and
analyzing the multi-color images to determine characteristics of the object,
15 wherein regions of the image at or near high gradients are analyzed differently from other regions of the image.

20 20. A method according to claim 19 wherein the regions at high gradients are excluded from analysis.

21. A method according to claim 19 or claim 20 wherein regions of relatively lower gradients are analyzed to determine characteristics of the object.

25 22. A method according to any of claims 19-21 wherein the object is a printed circuit board.

Docket No.
139/02059

Declaration and Power of Attorney For Patent Application

English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

INSPECTION OF PRINTED CIRCUIT BOARDS USING COLOR

the specification of which

(check one)

is attached hereto.

was filed on August 18, 1998 as United States Application No. or PCT International

Application Number PCT/IL98/00393

and was amended on February 2, 2000 and Herewith

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/>
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(Number)	(Country)	(Day/Month/Year Filed)	<input type="checkbox"/>

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States provisional application(s) listed below:

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

I hereby claim the benefit under 35 U. S. C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C. F. R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

(Application Serial No.)

(Filing Date)

(Status)

(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)

(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)

(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

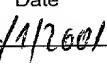
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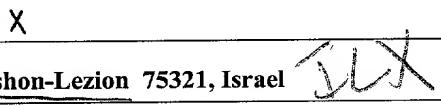
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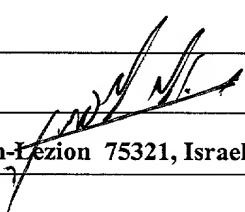
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